

31050 U.S. PTO  
10/022967



2/13/01

SC 737

# U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10022967	12/13/2001	438	770	2322 2942	Schilling

\*\*APPLICANTS: Falster Robert;

\*\*CONTINUING DATA VERIFIED:  
THIS APPLN CLAIMS BENEFIT OF 60/256,783 12/19/2000

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\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no  
35 USC 119 conditions met ☐ yes ☐ no  
Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO  
MEMC 98-6050 (2462.1)

TITLE : Process for reclaiming semiconductor wafers and reclaimed wafers

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Applicati n Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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